



Attorney Docket No.: P16857

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Parthasarathy, et al.

Application No: 10/731,996

Filed: December 9, 2003

For: Method and Apparatus for Balancing
Memory Access Latency and
Bandwidth

Examiner: Iwashko, Lev

Art Unit: 2186

Confirmation No.: 5869

Mail Stop: Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA. 22313-1450

AMENDMENT and REMARKS

Dear Sir:

In response to the Non-Final Office Action of February 6, 2006, please consider the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims, which begin on page 2 of this paper.

06/09/2006 BABRAHA1 00000034 10731996

01 FC:1202
02 FC:1201

150.00 OP
200.00 OP

Remarks begin on page 10 of this paper.